

### **Amendments to the Claims**

This listing of claims will replace all prior versions, and listings, of claims in the application.

All claims currently being amended are shown with deleted text struckthrough or double bracketed and new text underlined. Additionally, the status of each claim is indicated in parenthetical expression following the claim number.

Claims 21 – 40 remain.

Claims 31 - 35 are being cancelled without prejudice.

### **WHAT IS CLAIMED IS:**

1 – 9. (Cancelled)

23 – 24. (Cancelled)

31 – 35. (Cancelled)

36. (Original)      An amplifier for driving two speakers across a three – wire interface comprising:

    a first differential amplifier for receiving a pair of differential right channel signals referenced to a common mode voltage and outputting a right channel output signal representing a combination of a difference between the pair of differential right channel signals and a common mode output signal;

    a second differential amplifier for receiving a pair of differential left channel signals referenced to the common mode voltage and outputting a left channel output signal representing a combination of a difference between the differential left channel signals and the common mode output signal; and

    a third differential amplifier receiving a combination of a first one of the pair of

differential right channel signals, a first one of the pair of differential left channel signals and the common mode voltage, and outputting the common mode output signal.

37. (Original)        The amplifier of Claim 36, wherein the combination of a difference between the pair of differential right channel signals and the common mode output signal comprises a sum of the difference between the pair of differential right channel signals and the common mode output signal and the combination of the difference between the pair of differential left channel signals and a common mode output signal comprises a sum of the difference between the pair of differential left channel signals and the common mode output signal.

38. (Original)        The amplifier of Claim 36, wherein the first one of the pair of right differential channel signals is presented to a non – inverting input of the first differential amplifier and the first one of the pair of left differential channel signals is presented to a non – inverting input of the second differential amplifier.

39. (Original)        The amplifier of Claim 36, wherein the common mode output signal is fed – back to a non – inverting terminal of the first differential amplifier and a non – inverting terminal of the second differential amplifier.

40. (Original)        The amplifier of Claim 36, further comprising:  
                         a first digital to analog converter for generating the pair of differential right channel signals referenced to the common mode voltage; and  
                         a second digital to analog converter for generating the pair of differential left channel signals referenced to the common mode voltage.